Roll No. | | | | | | | | | | | | | Total No. of Pages : 02

Total No. of Questions : 07

B.Sc.(IT) (Sem.–3)

COMPUTER SYSTEM ARCHITECTURE

Subject Code : BS-201

Paper ID : [B0409]

Time : 3 Hrs. Max. Marks : 60

INSTRUCTION TO CANDIDATES :

1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.**

SECTION-A

l. Write short notes on :

a) Differentiate between Address space and Memory space.

b) What do you mean by Locality of Reference?

c) Name various registers available in basic computers.

d) What do you mean by Zero Address Instruction?

e) Give the full form of following abbreviations used in computer terminology :

IBIY, ENIAC, EMKL, UNIVAC

f) What is Instruction Pipelining?

g) Differentiate between RAM and ROM.

h) What do you mean by Paging?

i) What is Machine Language? Why is it required?

j) Differentiate between vectored and non-vectored interrupt.

SECTION-B

2) a) Write the functions performed by the computer system to handle input output devices.

   b) Explain program controlled data transfer.

3) Explain instruction format. What are the various types of instructions formats? Discuss them with suitable examples.

4) What is the design principle of virtual memory? Discuss the virtual address mapping scheme.

5) How computer instructions can be classified? Give the format along with the description.

6) List out the various computer generations along with their basic characteristics.

7) What is an Addressing Mode? Explain various addressing modes in detail.